

Angelina Schematic

Responsible : Stanly Liang

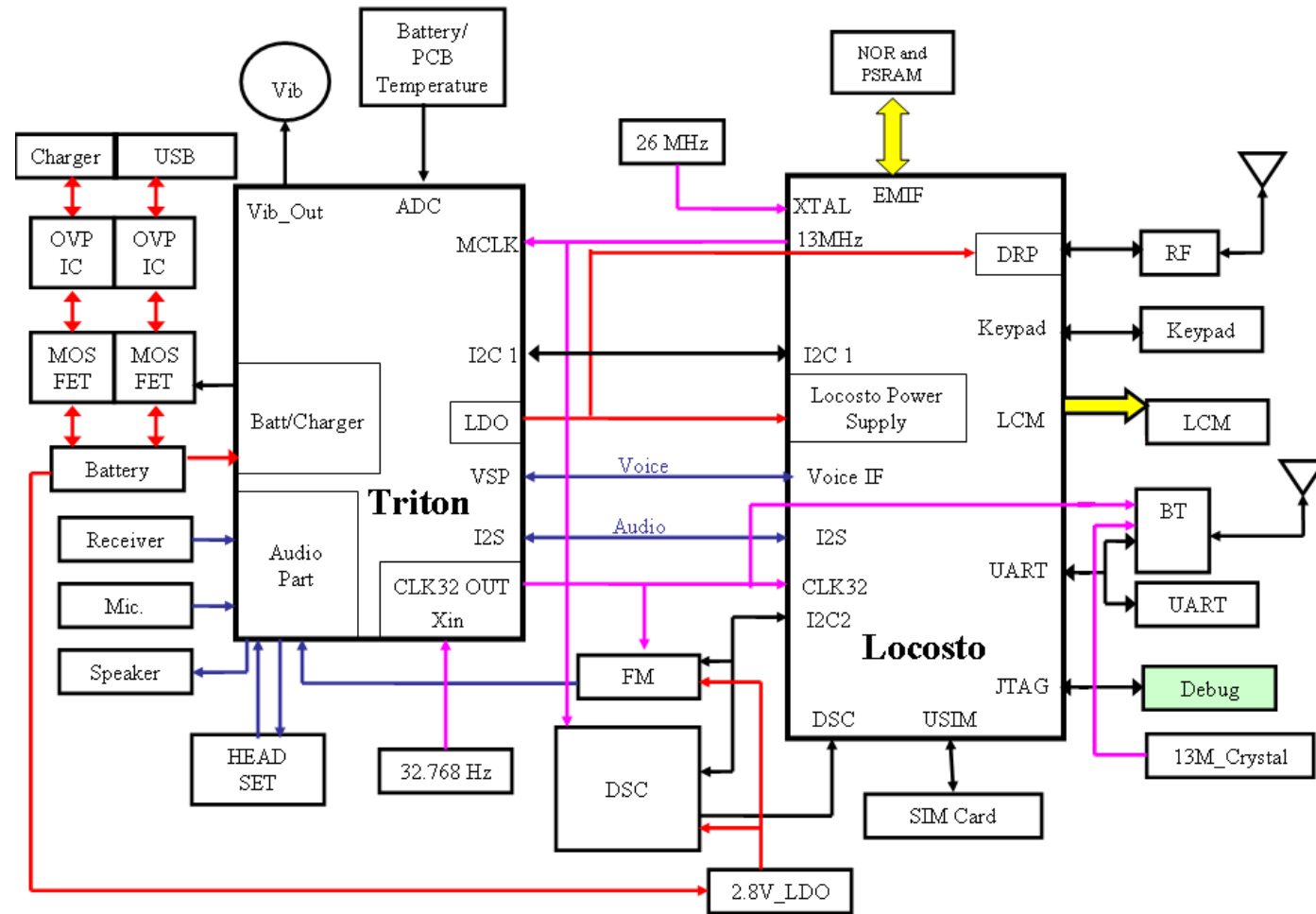
Approved by : Marvin Lee

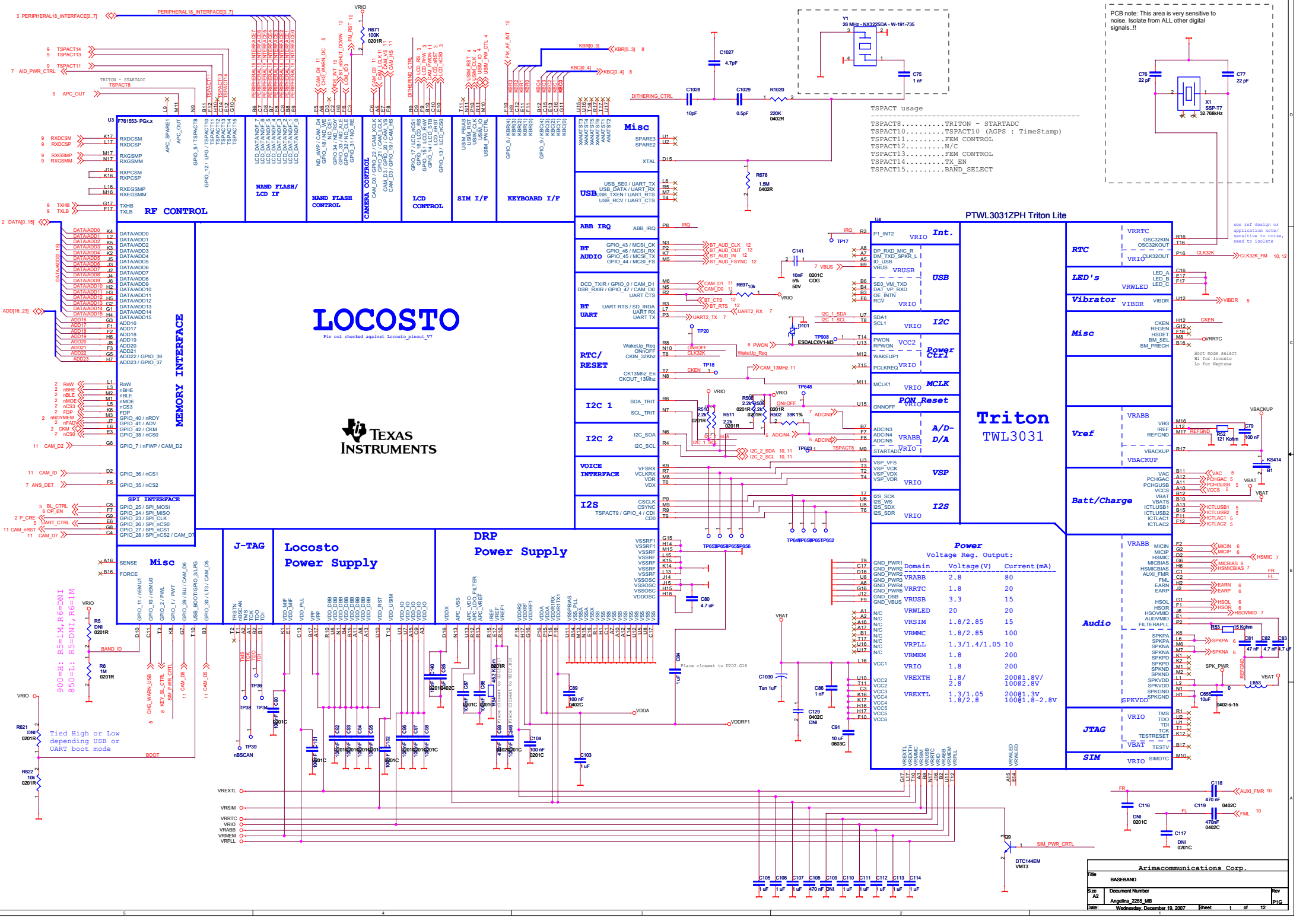
Revision : P1G

Date : 2007/12/18

PCB revision : 2253MB-005-D

Angelina Block Diagram





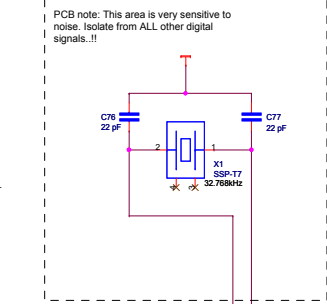
LOCOSTO

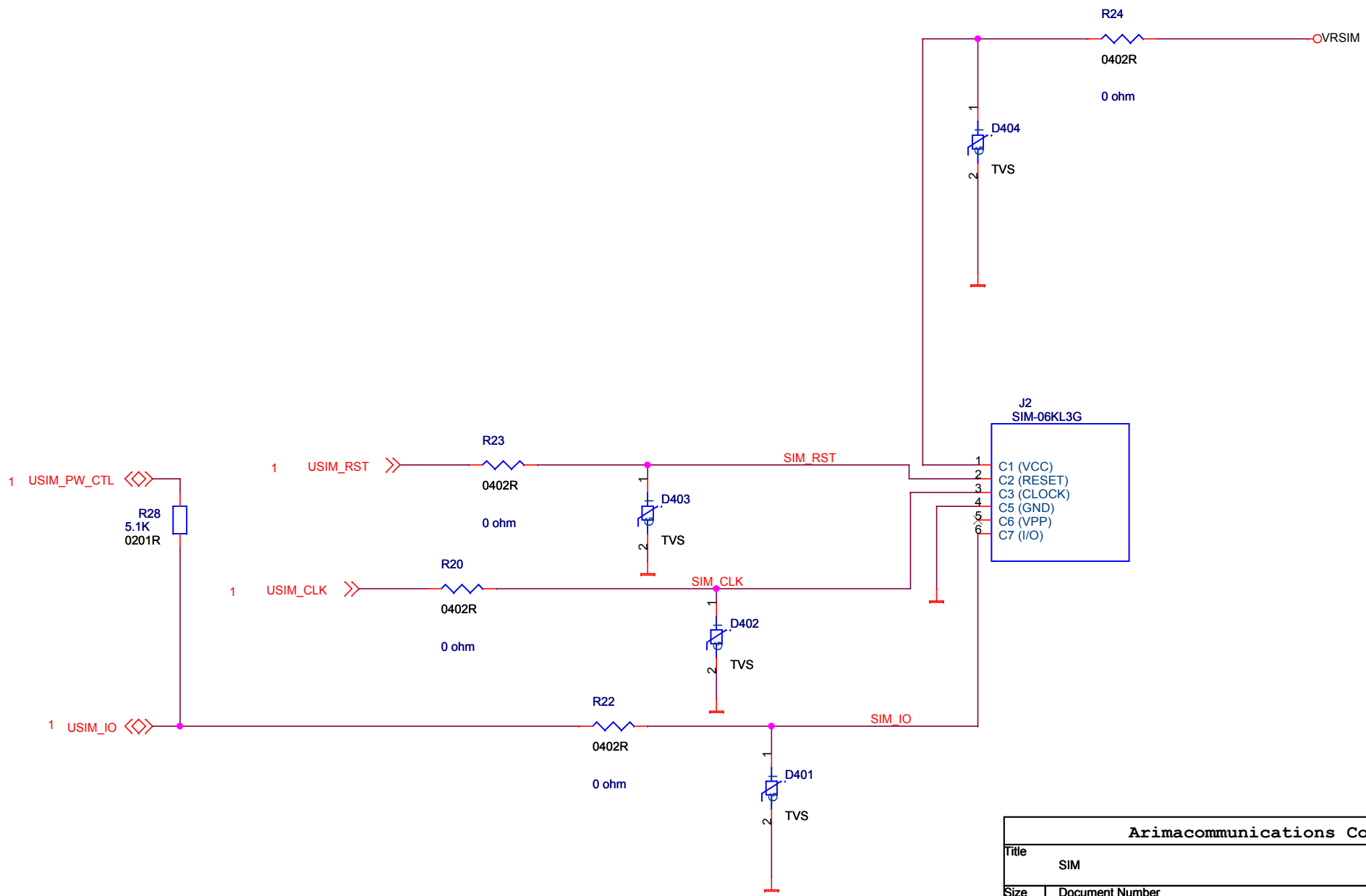
Pin out checked against locosto_pinout_v7



Triton
T3031

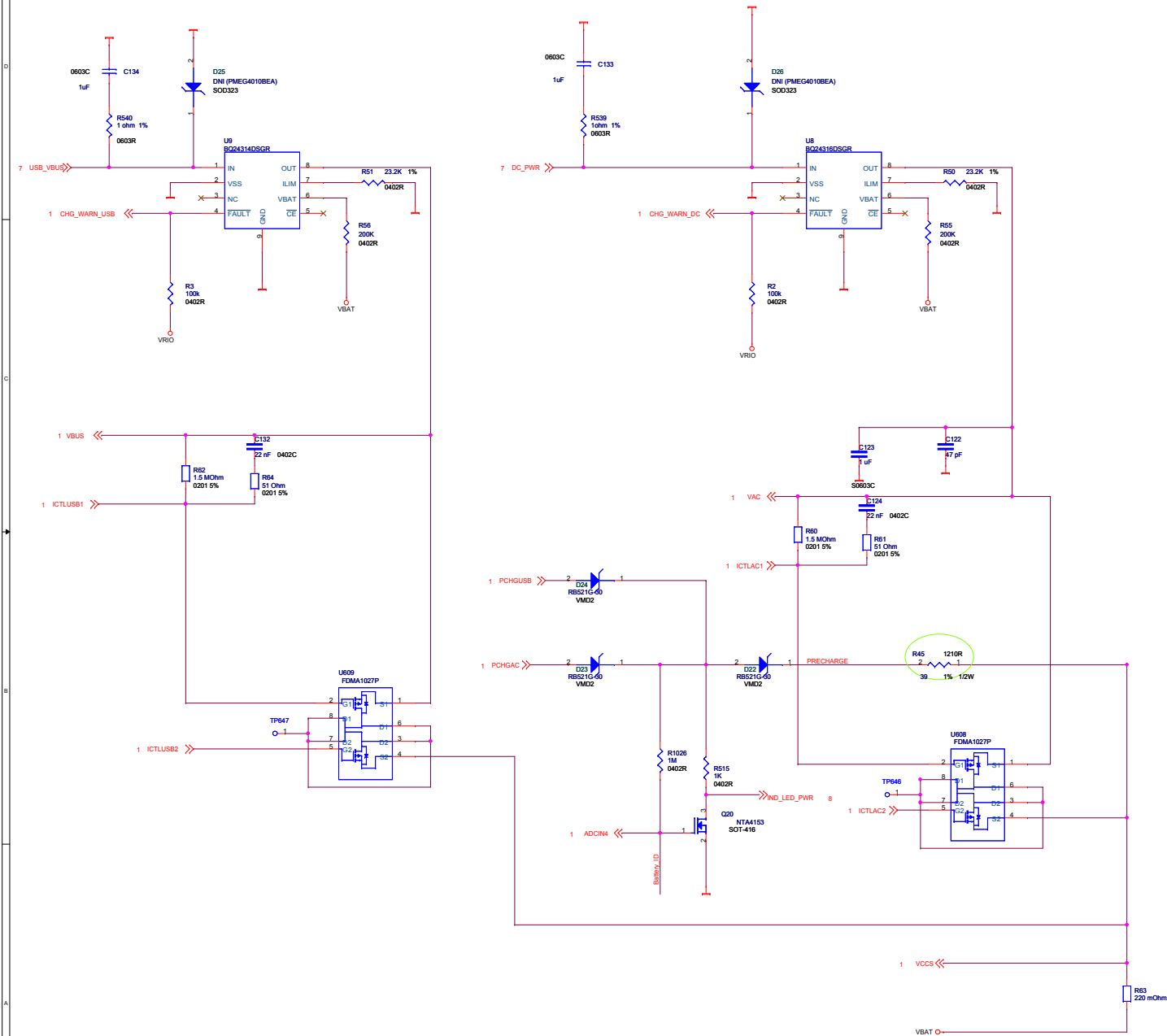
Power			
Voltage Reg. Output:			
Domain	Voltage (V)	Current (mA)	
VRABB	2.8	80	
VRVTT	1.8	20	
VRVSS	3.3	15	
VRVLE	20	20	
VRVSS	1.8/2.85	15	
VRVSS	1.8/2.85	100	
VRVLE	1.3/1.4/1.05	10	
VRVSS	1.8	200	
VRVSS	1.8	200	
VRVSS	1.8/2.8	20001.8V/10082.8V	
VRVSS	1.3/1.05	20001.3V/10081.8-2.8V	





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CHARGING

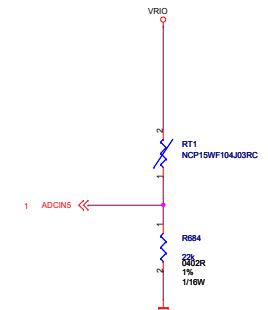


Rsense connected to VCCS and VBATS, must be as closed as possible to chip.

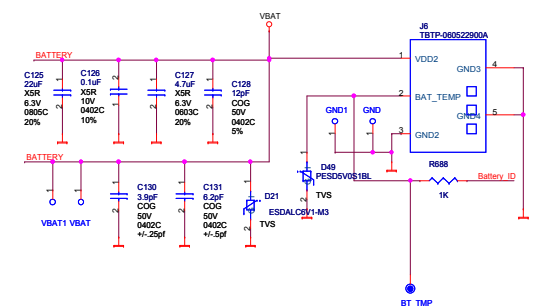
VIBRATOR

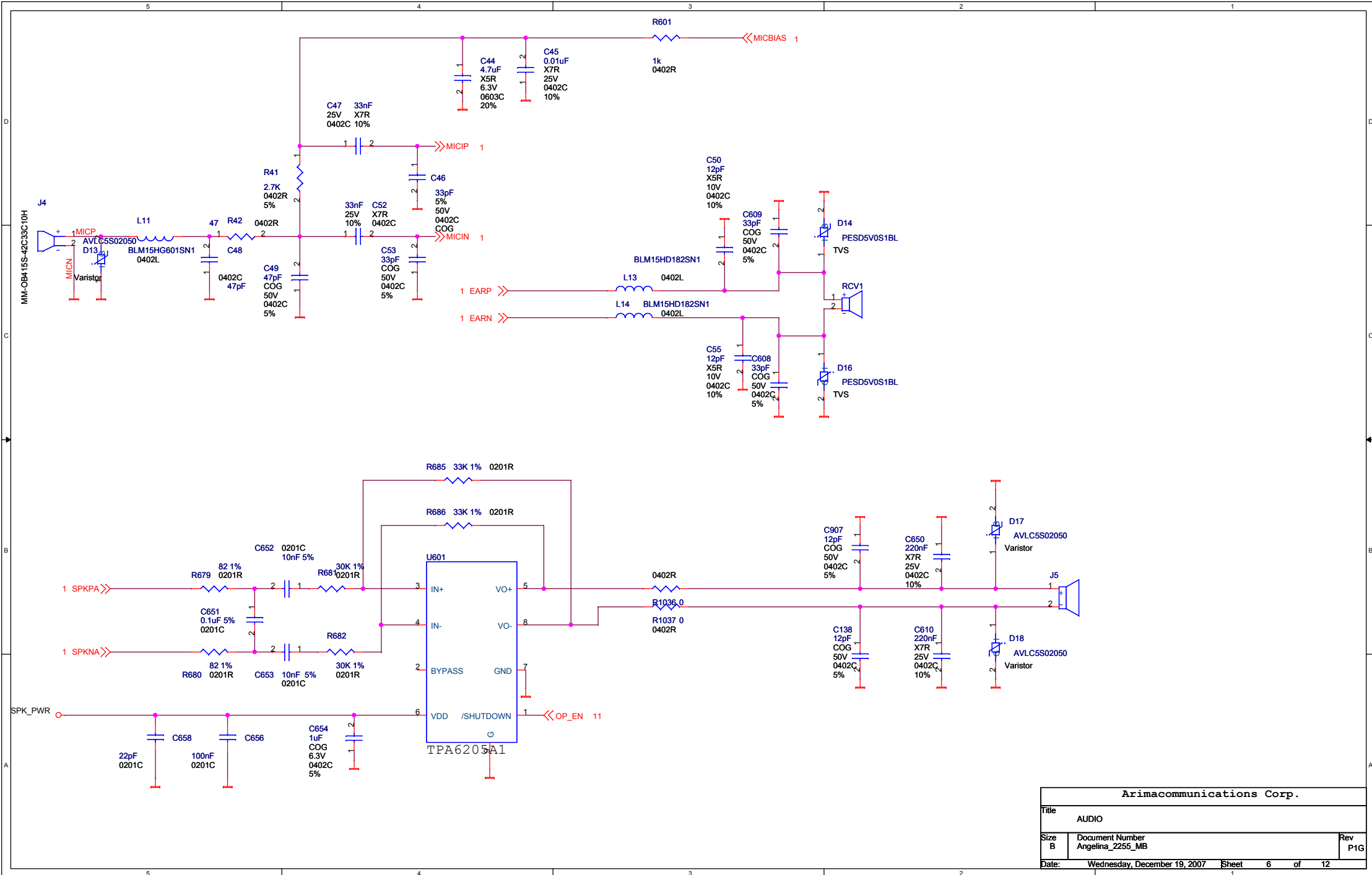


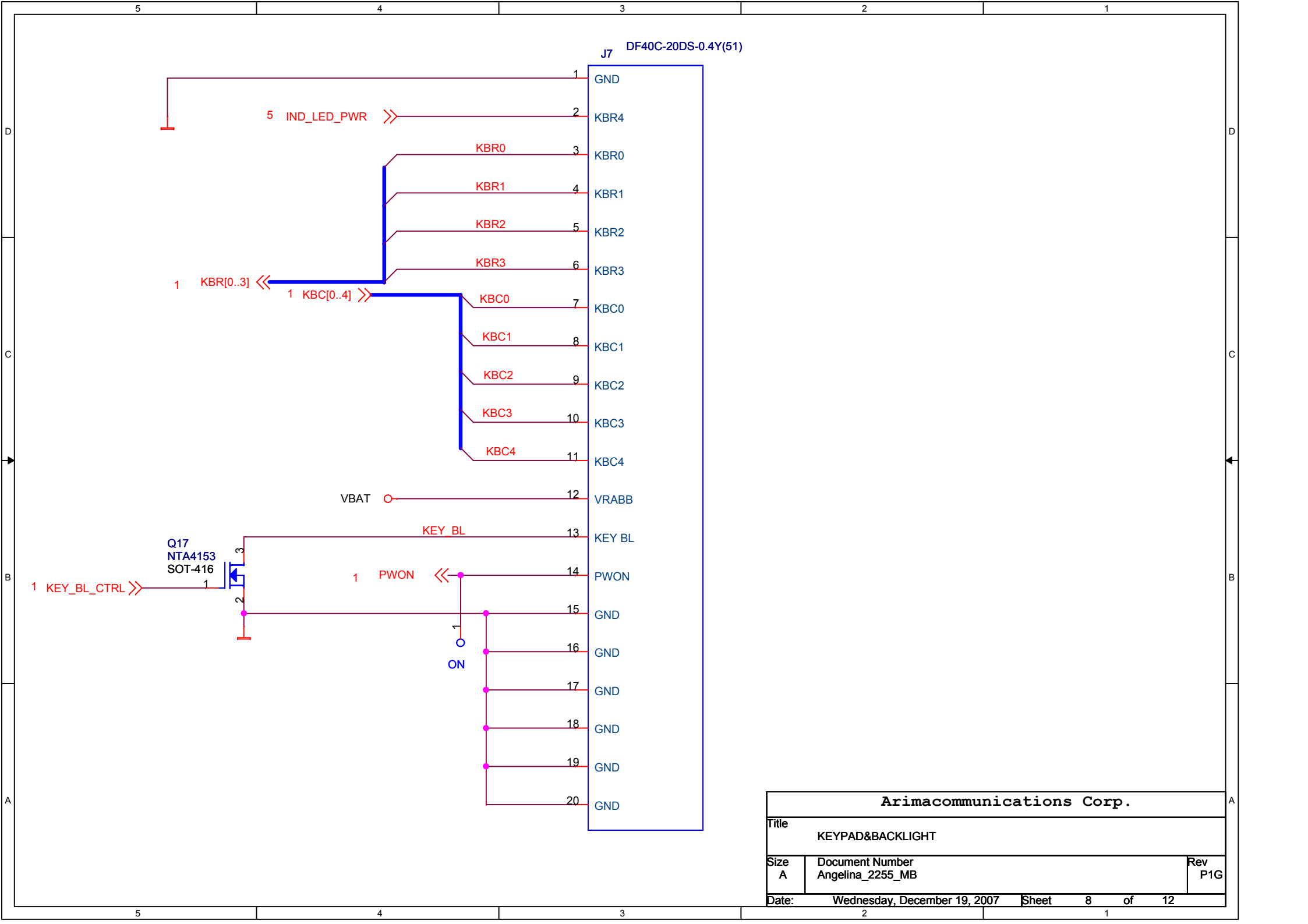
NTC

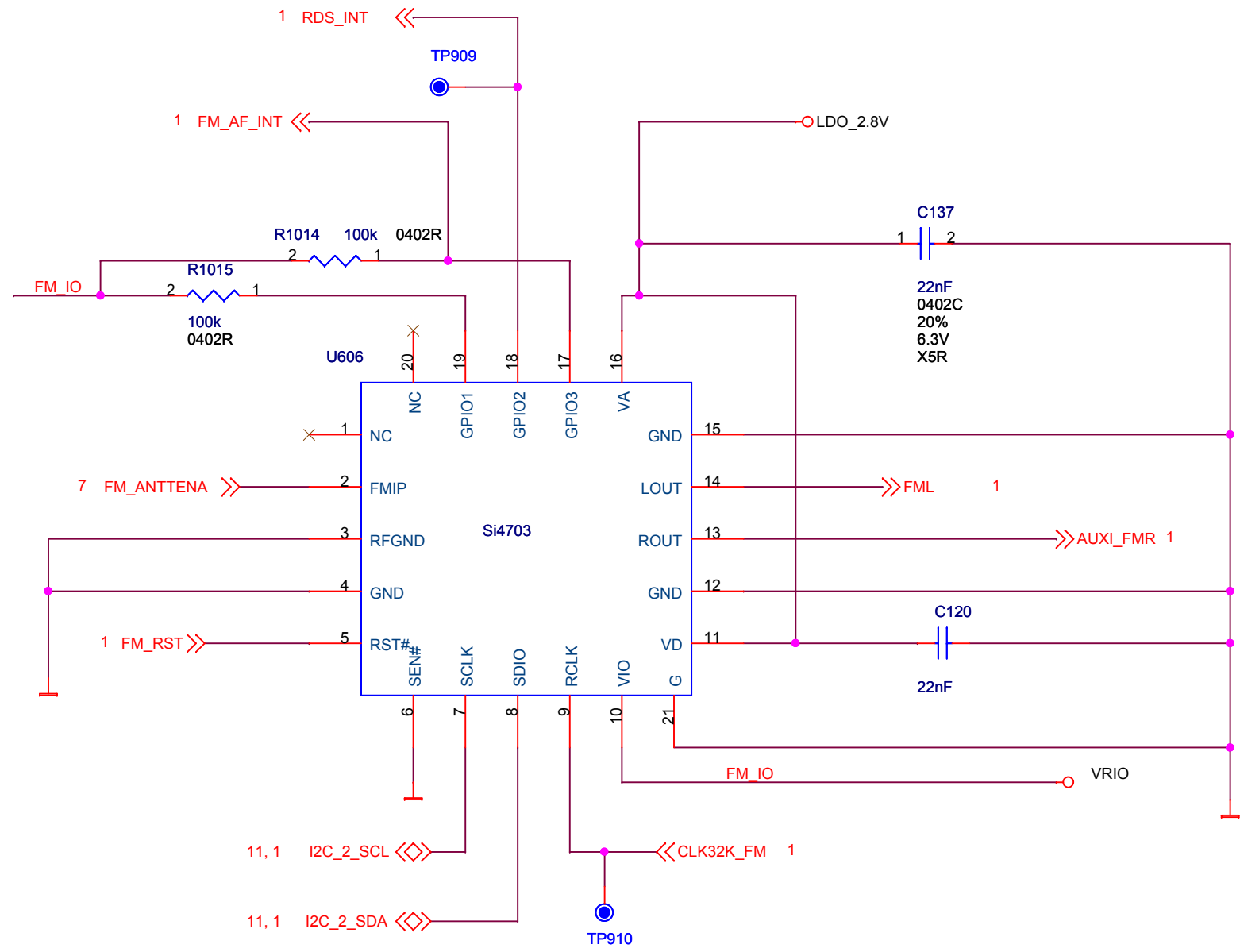


BATTERY CONNECTOR

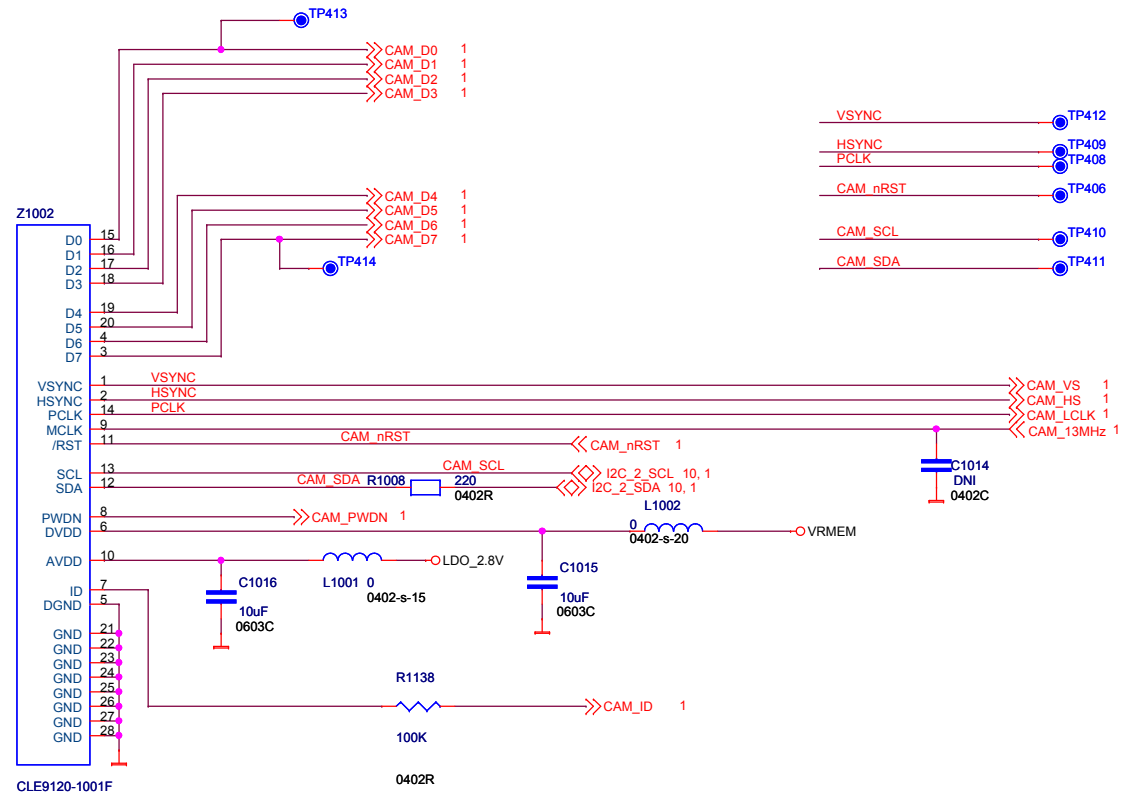






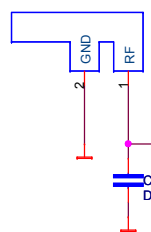


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FM RADIO		
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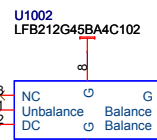
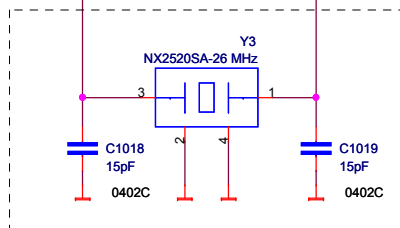


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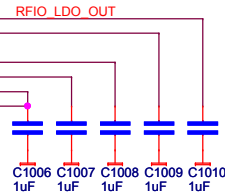
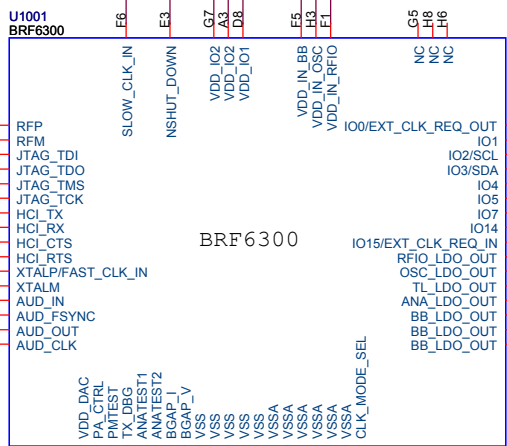
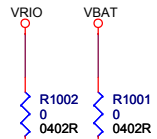
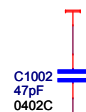
Bluetooth antenna
Z1001



7 BT_TX
7 BT_RX
1 BT_RTS
1 BT_CTS



1 BT_nSHUT_DOWN
1 CLK32K_FM
2.7 pF±0.1pF



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